

Portable power conversion applications present unique and challenging design considerations. Innovative, small electronics require solutions with small footprints. In order to maintain battery life, ...

Designing an ASIC chip for low power consumption is crucial to meet the needs of modern applications, ensuring longer battery life, reduced heat generation, and enhanced ...

This article serves as a comprehensive guide for professionals, offering actionable insights into the principles, tools, challenges, and future trends in low-power chip design.

Explore innovative approaches to power optimization in semiconductor design, including advanced power gating techniques, dynamic voltage scaling, and AI-assisted power analysis.

For many low-power battery-powered applications, standby power can be the primary impact to battery life, and it is critical that developers consider how the features required in the application impact the ...

In this spotlight article, we discuss not only the attributes of efficient power supplies, but also the types, design considerations, and some examples of how efficient power extends battery life ...

In most portable devices, the digital subsystem (processor plus digital I/O) consumes a large percentage of the total power, and in many of these systems the processor must remain on--even in a stand-by ...

Every year, more designers are required to make designs portable, wireless and energy efficient. This document seeks to simplify the transition to low-power applications by providing a ...

Minimization of power consumption in portable and battery-powered embedded systems has become an important aspect of processor and system design. Opportunities for power optimization and ...

Mobile devices, as well as battery-powered gadgets, require multiple power rails and multiple power domains. A power management solution based on discrete components adds ...

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